

11/05/01



Case No. 00CT18153314

DIRECTOR,  
U.S. PATENT AND TRADEMARK OFFICE  
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PATENT APPLICATIONS, ASSISTANT  
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Transmitted herewith for filing is the patent  
application of:

EXPRESS MAIL NO: EL747059590US

DATE OF DEPOSIT: November 5, 2001

NAME: Jennifer Ferguson

SIGNATURE: Jennifer Ferguson

11002 U.S. PTO  
10/008586



Inventors: Russell FRANCIS  
Michele ALIA

For: DEVICE AND METHOD FOR SELECTIVELY POWERING DOWN INTEGRATED  
CIRCUIT BLOCKS WITHIN A SYSTEM CHIP

**Enclosed are:**

[X] Patent Application: 19 pages, 11 claims is enclosed.  
[X] 5 sheets of drawings is enclosed.  
[X] The suggested drawing figure to be published is FIG. 4.  
[X] Citation Under 37 CFR 1.97 and PTO-1449 is enclosed.  
[X] A Preliminary Amendment is enclosed.  
[X] Submission of Proposed Drawing Modification.  
[X] Assignee info:  
Name: STMicroelectronics S.r.l.  
Address: Via C. Olivetti, 2, 20041 Agrate Brianza, Italy  
Country of Incorporation: Italy  
[X] Applicant claims priority benefit to the following foreign application(s):  
Country: Europe  
Application No.: 00830732.4  
Filing Date: November 3, 2000

The Declaration and Filing Fee are **NOT ENCLOSED**.

[X] Name, Address and Citizenship of Inventor(s) is as follows:

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[X] PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN

[X] Associate this file with Customer No. 27975.



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Date: November 5, 2001

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